#6264 P.003

PATENT 4-26-00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

FEB 1 7 2004

In re the Application of:

Serge Lasserre (TI-31354)

Serial No:

09/932,381

Filed: August 17, 2001

Art Unit: 2188

Examiner: Reginald G. Bragdo

RESPONSE UNDER 37 C.F.R. 1.116

EXPEDITED PROCEDURE EXAMINING GROUP 2188

Conf. No.: 4434

Local Memory With Indicator Bits to Support Concurrent DMA and CPU Access

AMENDMENT UNDER RULE 116

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Dear Sir:

Introductory Comments:

This paper is presented in response to the Office Action mailed on December 15, 2003. Entry of this amendment in this application, and reconsideration of the final rejection of claims in this application, are respectfully requested.

Proposed amendment to the specification is presented on page 2.

Proposed amendment to the claims begin on page 3.

Applicant's remarks begin on page 9.

To:

Technology Center 2188

Facsimile Number: 703.872.9306

Total Pages Sent 19

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Rodney M. Anderson From:

Anderson, Levine, and Lintel

(fax) 972.664.9606 (voice) 972.664.9554

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Lasserre

Serial No.: 09/932,381

Filed: August 17, 2001

Docket Number: TI-31354

Art Unit: 2188 Examiner: Bragdon

Local Memory With Indicator Bits to Support Concurrent DMA and **CPU Access**

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being transmitted by facsimile to the U.S. Patent and Trademark Office at 703.872.9306 on the date shown below:

Rodney M. Anderson

17 February 2004

FACSIMILE COVER SHEET

						
X FACSIMILE COVER SHEET NEW APPLICATION DECLARATION ASSIGNMENT FORMAL DRAWINGS INFORMAL DRAWINGS CONTINUATION APP'N DIVISIONAL APP'N		_X AMENDMENT UNDER R116 (1 EOT NOTICE OF APPEAL APPEAL ISSUE FEE REPLY BRIEF (IN TRIPLICATE) X FEE TRANSMITTAL (1 page)				
NAME OF INVENTOR(S): Lasserre TITLE OF INVENTION: Local Memory With Indicator Bits to Support Concurrent DMA and CPU Access TIFILE NO: THATIST DEPOSIT ACCT. NO.: THATIST 20-0668 FAXED: 17 February 2004 DUE: 17 February 2004 ATTY/SECY: ALL/RMA		RECEIPT DATE & SERIAL NO.: Serial No.: 09/932,381 Filing Date: 8/17/01				

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> Texas Instruments Incorporated PO Box 655474, M/S 3999 Dallas, TX 75074

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Applicant claims small entity status. See 37 CFR 1.27					2188			
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